

# A Probabilistic Model for Parametric Fairness in Isabelle/HOL <sup>\*</sup>

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**Abstract.** In paper [1], a liveness proof method suitable for inductive protocol verification is proposed. The utility of this method has been confirmed by several machine checked formal verifications[2–4]. One remaining question about [1] is the meaning of *Parametric Fairness*, a new fairness notion adapted from Pnueli’s *Extreme Fairness*[5] to suit the setting of higher-order logic. This paper tries to answer this question. As a standard practice in establishing a fairness notion, this paper constructs a probabilistic model for parametric fairness in Isabelle/HOL. Using this model, it is shown that most infinite executions of a concurrent system are parametrically fair. Therefore the definition of parametric fairness in paper [1] is reasonable. This work gives a firmer basis for existing and forthcoming formal verifications based on the method of paper [1].

**Keywords :** Liveness Proof, Inductive Protocol Verification, Probabilistic Model, Parametric Fairness.

## 1 Introction

Paulson’s inductive approach for protocol verification[6] has been used to verify fairly complex security protocols [7, 8]. The success gives incentives to extend this approach to a general approach for concurrent system verification. To achieve this goal, a method for the verification of liveness properties is needed.

In paper [1], a liveness proof method suitable for inductive protocol verification is proposed. In [1], proof rules for liveness properties (both response and reactivity) are derived. The proof rules are used to reduce the proof of liveness properties to the proof of safety properties, so that the original inductive approach’s advantages in the proof of safety properties can be fully exploited. The utility of this method has been confirmed by several machine checked formal verifications[2–4].

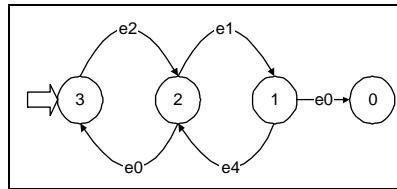
Paper [1] uses a new notion of fairness – *Parametric Fairness*, which is an adaption of Pnueli’s *Extreme Fairness*[5] to suit the setting of higher-order logic. As a standard practice in establishing a fairness notion, this

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paper constructs a probabilistic model for *Parametric Fairness*. Using this model, it is shown that most infinite executions of a concurrent system are parametrically fair. Therefore the definition of parametric fairness given in [1] is reasonable. The purpose of this paper is to give a firmer basis for existing and forthcoming formal verifications based on the method of paper [1].

As pointed out in [5], standard fairness notions like *weak fairness* and *strong fairness* are not adequate for showing some obvious liveness properties. Consider the concurrent system in Figure 1. It is intuitively obvious that once the system is in initial state 3, it will eventually get into the final state 0, supposing all actions in the system are fairly treated. However, both *weak fairness* and *strong fairness* fail to capture such an intuitive notion. For example, the infinite execution  $(e_2. e_1. e_4. e_0)^\omega$  is fair according to both *weak fairness* and *strong fairness*. The problem with *weak fairness* and *strong fairness* is that they only stress the fair treatment of actions, without considering the state under which these actions are taken. In execution  $(e_2. e_1. e_4. e_0)^\omega$ , even all actions in the system, i.e.  $e_0, e_1, e_2, e_4$ , are fairly treated, but it is still not strong enough to force the system into state 0, therefore the intuitively obvious liveness property mentioned above does not hold under this standardly fair execution.



**Fig. 1.** The diagram of a concurrent system

To solve this problem, paper [5] proposes the notion of extreme fairness which requires all combinations of states and actions be fairly treated. In Figure 1, this amounts to requiring all pairs in  $\{0, 1, 2, 3\} \times \{e_0, e_1, e_2, e_4\}$  be fairly treated. The above standardly fair execution  $(e_2. e_1. e_4. e_0)^\omega$  is not extremely fair, because the pair  $(1, e_0)$  is not fairly treated. To specify that every state-action pair be fairly treated in higher-order logic, we have to quantify universally over the type of state predicates. This universal quantification turned out to be problematic, because for any infinite execution  $\sigma$ , we can always construct a higher-order state predicate  $\varphi_\sigma$  which is not fairly treated in  $\sigma$ . By instantiating the universally quantified variable to  $\varphi_\sigma$ , it can be shown that execution  $\sigma$  is not fair. Therefore the universal quantification over the type of state predicates

makes the straightforward formulation of extreme fairness so restrictive that almost no execution can satisfy it. The details of this construction can be found in paper [1].

As a solution, paper [1] proposes *Parametric Fairness* as a refinement of *extreme fairness*. In parametric fairness, the list of state-action pairs which need to be fairly treated is explicitly given as a parameter to the fairness definition. Because in any concrete liveness proof, the state-action pairs which need to be fairly treated can always be given explicitly, the notion of parametric fairness achieves the same proof theoretical power as extreme fairness without being too restrictive semantically.

One remaining question about parametric fairness is whether the notion is liberal enough to accommodate most infinite executions of the underlying concurrent system. This paper answers the question by showing that if the nondeterminacy in concurrent executions is resolved by random choice, a probability space can be constructed with the execution set as basis, and that the set of parametrically fair executions is measurable and has probability 1. The result means that almost all concurrent executions are parametrically fair.

Similar works have already been done for extreme fairness and other notions of fairness[5, 9, 10], but none is in the setting of higher-order logic. Our contribution is to show that the formulation of parametric fairness in higher-order logic still has the desired property. Additionally, our work is more reliable because all preceding works are pencil and paper ones done outside object logic, while ours is inside the object logic Isabelle/HOL, machine-checked and coexisting with concrete verifications. Parametric fairness has the same motivation as extreme fairness, i.e. to reflect the probabilistic nature of concurrent executions. This paper shows that the goal is indeed achieved. Therefore the liveness proof method proposed in [1] is now on a firmer basis.

The rest of this paper is organized as follows: Section 2 introduces the notion of parametric fairness. Section 3 introduces the notion of probabilistic execution and constructs a probability space on sets of infinite executions. Section 4 assigns a probabilistic meaning to parametric fairness by showing that the probability of the set of fair executions equals to 1. Section 5 summarizes related works. Section 6 concludes.

## 2 The notion of parametric fairness

### 2.1 Concurrent systems

In inductive approach, system states are identified with finite executions, which are represented as lists of events. These events are arranged in reverse order of happening, the decision of which event to happen next is decided according to current system state. Formal definitions for concurrent system is given in Fig. 2, where the type of events is a polymorphic type ' $a$ ', and the type of system states is ' $a$  list'. We identify system states with finite execution traces. The terms *system state* and *finite execution* are used interchangeably. Both of them are written as  $\tau$ ,  $\tau'$ ,  $\tau_1$ ,  $\tau_2$ , etc.

```

constdefs i-th :: (nat ⇒ 'a) ⇒ nat ⇒ 'a (- [64, 64] 1000)
σi ≡ σ i

consts prefix :: (nat ⇒ 'a) ⇒ nat ⇒ 'a list ([ ]- [64, 64] 1000)
primrec [σ]0 = []
[σ](Suc i) = σi # [σ]i

constdefs may-happen :: 
  'a list ⇒ ('a list × 'a) set ⇒ 'a ⇒ bool (- [-> - [64, 64] 50)
τ [cs> e ≡ (τ, e) ∈ cs

consts vt :: ('a list × 'a) set ⇒ 'a list set
inductive vt cs
intros
vt-nil [intro] : [] ∈ vt cs
vt-cons [intro] : [τ ∈ vt cs; τ [cs> e] ⇒ (e # τ) ∈ vt cs] ⇒ (e # τ) ∈ vt cs

consts derivable :: 'a ⇒ 'b ⇒ bool (- ⊢ - [64, 64] 50)
defs (overloaded)
  fint-valid-def: cs ⊢ τ ≡ τ ∈ vt cs
  inf-valid-def: cs ⊢ σ ≡ ∀i. [σ]i [cs> σi

```

**Fig. 2.** The definitions of concurrent system

The type of infinite executions is  $nat \Rightarrow 'a$ , which is often abbreviated as ' $a$  seq'. For an infinite execution  $\sigma$ , the event happens at the  $i$ -th step is  $\sigma_i$ , which is also written as  $\sigma_i$ . The first  $i$  events of an infinite execution  $\sigma$  can be packed into a list, which exactly forms a finite execution. Such a packing is written as  $[\sigma]_i$ , which is also called a *prefix* of  $\sigma$ .

A concurrent system is often written as  $cs$ , and its type is (' $a$  list × ' $a$ ) set. The expression  $(\tau, e) \in cs$  means that the event  $e$  is legitimate to happen under state  $\tau$  according to  $cs$ . The notation  $(\tau, e) \in cs$  is also written as  $\tau [cs> e$ . The set of *valid* finite executions of  $cs$  is written as  $vt cs$ . The expression  $\tau \in vt cs$  is also written as  $cs \vdash \tau$ . The operator  $\vdash$  is overloaded, so that  $\sigma$  is a valid infinite execution of  $cs$  can be written as  $cs \vdash \sigma$ . An infinite execution  $\sigma$  is valid under  $cs$  iff all of its prefixes are valid.

## 2.2 Embedding LTL

LTL (Linear Temporal Logic) is widely used for the specification and verification of concurrent systems. A shallow embedding of LTL is given in Fig.3. In this paper, LTL is used to express various temporal properties of infinite executions, including liveness properties.

```

types 'a tlf = (nat ⇒ 'a) ⇒ nat ⇒ bool

consts valid-under :: 'a ⇒ 'b ⇒ bool (- ⊨ - [64, 64] 50)
defs (overloaded) pr ⊨ φ ≡ let (σ, i) = pr in φ σ i
defs (overloaded) σ ⊨ φ ≡ (σ:nat ⇒ 'a, (0:nat)) ⊨ φ

constdefs always :: 'a tlf ⇒ 'a tlf (□- [64] 65)
  □φ ≡ λ σ i. ∀ j. i ≤ j → (σ, j) ⊨ φ

constdefs eventually :: 'a tlf ⇒ 'a tlf (◊- [64] 65)
  ◊φ ≡ λ σ i. ∃ j. i ≤ j ∧ (σ, j) ⊨ φ

```

**Fig. 3.** A shallow embedding of LTL

The type of LTL formulae is defined as '*a tlf*'. The expression  $(\sigma, i) \models \varphi$  means that LTL formula  $\varphi$  is valid at the  $i$ -th step of  $\sigma$ . The operators *always*  $\square$  and *eventual*  $\diamond$  are defined literally.

## 2.3 Introduction of parametric fairness

In paper [1], two liveness proof rules are derived. The one for response properties is:

$\llbracket RESP \: cs \: F \: E \: N \: P \: Q; \: cs \vdash \sigma; \: PF \: cs \: \{F, \: E, \: N\} \: \sigma \rrbracket \implies \sigma \models \square \langle \psi \rangle \rightarrow \diamond \langle \varphi \rangle$

and the one for reactivity properties is:

$\llbracket REACT \: cs \: F \: E \: N \: P \: Q; \: cs \vdash \sigma; \: PF \: cs \: \{F, \: E, \: N\} \: \sigma \rrbracket \implies \sigma \models \square \diamond \langle \psi \rangle \rightarrow \square \diamond \langle \varphi \rangle$

To prove a liveness result, an execution path must be found, which goes from the starting state characterized by  $\psi$  to the ending state characterized by  $\varphi$ . Such a path is represented by a list of (state predicate, event function)-pairs, which needs to be fairly treated, so that the execution of the concurrent system will eventually go along the path. The path is abstract in the sense that instead of concrete (state, event)-pairs, it consists of (state predicate, event function)-pairs, where the state predicate specifies the condition under which the event yielded by the event function is to happen. The functions  $F$  and  $E$  are given by verification staff to generate the state predicates and event functions respectively. The  $N$  is the length of the path. The expression  $\{F, E, N\}$  represents the generated (state predicate, event function)-pair list. The pair list should be properly designed to form a chain leading from  $\psi$  to  $\varphi$ . Such a requirement is expressed in the definitions of both *RESP* and *REACT*[1].

The purpose of this paper is to give a probabilistic meaning to the remaining  $PF \{F, E, N\} \sigma$ , which requires that the infinite execution  $\sigma$  is parametrically fair with respect to the parameter  $\{F, E, N\}$ . The formal definition of  $PF$  is given in Fig. 4.

$EF_\alpha :: ('a \text{ list} \times 'a) \text{ set} \Rightarrow ('a \text{ list} \Rightarrow \text{bool}) \Rightarrow ('a \text{ list} \Rightarrow 'a) \Rightarrow (\text{nat} \Rightarrow 'a) \Rightarrow \text{bool}$ $EF_\alpha \text{ cs } P \text{ E } \sigma \equiv$ $\sigma \models \square \diamond (\lambda \sigma. i. P \llbracket \sigma \rrbracket_i \wedge \llbracket \sigma \rrbracket_i \text{ [cs>} E \llbracket \sigma \rrbracket_i]) \longrightarrow \sigma \models \square \diamond (\lambda \sigma. i. P \llbracket \sigma \rrbracket_i \wedge \sigma_i = E \llbracket \sigma \rrbracket_i)$
$EF :: ('a \text{ list} \times 'a) \text{ set} \Rightarrow (\text{nat} \Rightarrow 'a) \Rightarrow \text{bool}$ $EF \text{ cs } \sigma \equiv \forall P. E. EF_\alpha \text{ cs } P \text{ E } \sigma$
<b>types</b> $'a \text{ pe} = ('a \text{ list} \Rightarrow \text{bool}) \times ('a \text{ list} \Rightarrow 'a)$
$PF :: ('a \text{ list} \times 'a) \text{ set} \Rightarrow 'a \text{ pe list} \Rightarrow (\text{nat} \Rightarrow 'a) \Rightarrow \text{bool}$ $PF \text{ cs } pel \sigma \equiv \text{list-all } (\lambda (P, E). EF_\alpha \text{ cs } P \text{ E } \sigma) \text{ pel}$

**Fig. 4.** Different notions of fairness

In Fig. 4, expression  $EF_\alpha \text{ cs } P \text{ E } \sigma$  specifies that state predicate  $P$  and event function  $E$  are fairly treated in execution  $\sigma$ , expression  $EF \text{ cs } \sigma$  is the literal translation of extreme fairness, which requires *every* state predicate and event function being fairly treated. This definition is shown in [1] to be too restrictive. The definition of  $PF \text{ cs } pel \sigma$  represents our remedy, which only requires the (state predicate, event function)-pairs appearing in parameter  $pel$  being fairly treated. In the following, we are going to construct a measure space over the power set of all infinite executions, and show that *almost all* infinite executions satisfy the requirement of  $PF$ . However, since our approach works at meta level, the premise  $PF \text{ cs } \{F, E, N\} \sigma$  can not be removed from the final results. It is there to provide people with information that the liveness result is achieved through the enabling execution path  $\{F, E, N\}$ .

When a complex concurrent system is verified, people usually need to derive many liveness results, each with its own enabling path. The result in this paper also means that the requirement of  $PF \text{ cs } pel \sigma$ , using the conjunction of all these enabling paths as the parameter  $pel$ , can still be satisfied by *almost all* infinite executions.

### 3 Probability space construction

#### 3.1 Formalizing probabilistic execution

To model random choice, we introduce a function  $R$ , where  $R(\tau, e)$  is the probability of event  $e$  being chosen to happen under system state  $\tau$ . Therefore,  $R(\tau, e) = 0$  means event  $e$  is not eligible to happen under

state  $\tau$ , while  $R(\tau, e) > 0$  means  $e$  is eligible to happen with  $R(\tau, e)$  as the happening probability. Every  $R$  represents an execution strategy of a concurrent system. The underlying concurrent system can be defined in terms of  $R$  as  $CS \equiv \{(\tau, e). 0 < R(\tau, e)\}$ . The set of events eligible to happen under state  $\tau$  is given as  $N\tau \equiv \{e. 0 < R(\tau, e)\}$ .

It is natural to assert the following axioms:

1.  $0 \leq R(\tau, e) \wedge R(\tau, e) \leq 1$ , which is a routine requirement of probability theory.
2.  $CS \vdash \tau \longrightarrow (\sum e \in N\tau. R(\tau, e)) = 1$ . The purpose of this axiom is to fulfill the standard probability theory requirement that the summation of all possible outcomes must equal to 1. This axiom also entails that every valid finite execution can always be extended by at least one event. By introducing a *Tick* event, which represents the ticks of a system wide clock, this requirement can be satisfied easily, since nothing can prevent time from advancing. This axiom also entails *finite* ( $N\tau$ ), which means that the underlying concurrent system  $CS$  is finitely branching.
3.  $\forall \tau e. 0 < R(\tau, e) \longrightarrow bnd \leq R(\tau, e)$ , where  $bnd$  is the lower bound of all nonzero  $R$ -probabilities. It is natural to require that  $0 < bnd \wedge bnd < 1$ .

Function  $R$  induces a measure function  $\pi$  on finite executions:

$$\begin{aligned}\pi[\ ] &= 1 \\ \pi(e\#\tau) &= R(\tau, e) * \pi\tau\end{aligned}$$

For any valid system state  $\tau$ , we have  $\pi\tau > 0$ . For any valid infinite execution  $\sigma$ , we have  $\forall i. \pi[\sigma]_i > 0$ . The base set(or sample space), on which we are going to construct the probability space, is defined as:

$$Path \equiv \{\sigma. (\forall i. \pi[\sigma]_i > 0)\}$$

It can be shown that  $(CS \vdash \sigma) = (\sigma \in Path)$ , i.e.  $Path$  coincides with the set of all valid infinite executions of the concurrent system  $CS$ .

### 3.2 Outline of the construction

The definition of probability space is given in Fig. 5. A probability space is defined to be a measure space  $(U, F, Pr)$ , where  $U$  is the base set,  $F$  the family of measurable sets (also called measurables),  $Pr$  the measure function. For a probability space  $(U, F, Pr)$ , the measure of the base set must be 1. The definition of measure space uses standard notions

such as  $\sigma$ -algebra, positivity and countable additivity. In the definition of countable additivity, we use Isabelle library function *sums*, where '*f sums c*' stands for  $\sum_{n=0}^{\infty} f(n) = c$ . The rest of the definitions are self-explaining.

```

consts algebra :: ('a set × 'a set set) ⇒ bool
algebra (U, F) = (F ⊆ Pow(U) ∧ {} ∈ F ∧ (∀a ∈ F. (U - a) ∈ F) ∧
(∀a b. a ∈ F ∧ b ∈ F → a ∪ b ∈ F))

consts sigma-algebra :: ('a set × 'a set set) ⇒ bool
sigma-algebra(U, F) = (F ⊆ Pow(U) ∧ U ∈ F ∧ (∀a ∈ F. U - a ∈ F) ∧
(∀a. (∀ i::nat. a(i) ∈ F) → (∪i. a(i)) ∈ F))

consts positive:: ('a set set × ('a set ⇒ real)) ⇒ bool
positive(F, Pr) = (Pr {} = 0 ∧ (∀A. A ∈ F → 0 ≤ Pr A))

consts countably-additive:: ('a set set × ('a set ⇒ real)) ⇒ bool
countably-additive(F, Pr) =
(∀f::(nat ⇒ 'a set). range(f) ⊆ F ∧
(∀m n. m ≠ n → f(m) ∩ f(n) = {}) ∧
(∪i. f(i)) ∈ F
→ (λn. Pr(f(n))) sums Pr (∪i. f(i)))

consts measure-space:: ('a set × 'a set set × ('a set ⇒ real)) ⇒ bool
measure-space (U, F, Pr) =
(sigma-algebra (U, F) ∧ positive (F, Pr) ∧ countably-additive (F, Pr))

consts prob-space:: ('a set × 'a set set × ('a set ⇒ real)) ⇒ bool
prob-space (U, F, Pr) = (measure-space (U, F, Pr) ∧ Pr U = 1)

```

**Fig. 5.** Definition of probability space

The notion of  $\sigma$ -algebra is a generalization of algebra. As shown in Fig. 5, the only difference is that algebra is closed under finite union, while  $\sigma$ -algebra is closed under countable union. A standard way to obtain  $\sigma$ -algebra is to construct an algebra  $(U, F)$  first, then use operator *sigma* to generate a  $\sigma$ -algebra  $(U, \sigma(U, F))$ . The definition of *sigma* is:

```

consts sigma :: ('a set × 'a set set) ⇒ 'a set set
inductive sigma M intros
  basic: (let (U, A) = M in (a ∈ A)) ⇒ a ∈ sigma M
  empty: {} ∈ sigma M
  complement: a ∈ sigma M ⇒ (let (U, A) = M in U - a) ∈ sigma M
  union: (∧i::nat. a i ∈ sigma M) ⇒ (∪i. a i) ∈ sigma M

```

According to *Carathéodory's extension theorem*[11, 12], a measure *Pr* on *F* can be generalized naturally to a measure *Pr'* on *sigma(U, F)*, so that  $(U, \sigma(U, F), \Pr')$  forms a measure space. The presentation of this extension theorem in Isabelle/HOL is as follows:

```

[[algebra (U, F); positive (F, Pr); countably-additive (F, Pr)]]
⇒ ∃P. (∀A. A ∈ F → P A = Pr A) ∧ measure-space (U, sigma (U, F), P)

```

This theorem suggests the way we are going to construct the probability space for infinite executions. The base set is *Path*, on top of which an alge-

bra  $PA$  will be defined. A measure  $\mu$  on  $PA$  will be given with  $\mu(Path)=1$ . Using extension theorem, we can then get the desirable probability space.

### 3.3 An algebra of infinite execution sets with measure $\mu$

The set of infinite executions with finite execution  $\tau$  as prefix is defined as follows:

$$palg\text{-}embed \tau \equiv \{\sigma \in Path. \llbracket \sigma \rrbracket_{|\tau|} = \tau\}$$

The  $|\tau|$  in this definition is the length of  $\tau$ .

A set of infinite executions is said to be supported by a list of finite executions  $[\tau_0, \tau_1, \tau_2, \dots, \tau_n]$ , if every infinite execution in the set is prefixed by some  $\tau_i$  among  $\tau_0, \tau_1, \tau_2, \dots, \tau_n$ . It is easy to see that the set supported by  $[\tau_0, \tau_1, \tau_2, \dots, \tau_n]$  is  $\bigcup_{i=0}^n (palg\text{-}embed \tau_i)$ . But we write the supported set in a slightly different way as  $palgebra\text{-}embed([\tau_0, \tau_1, \tau_2, \dots, \tau_n])$ , which is defined as the following:

```
consts palgebra\text{-}embed :: 'a list list ⇒ 'a seq set
primrec
  palgebra\text{-}embed [] = {}
  palgebra\text{-}embed (τ#l) = (palg\text{-}embed τ) ∪ palgebra\text{-}embed l
```

The subset family  $PA$  mentioned earlier is defined as:

$$PA \equiv \{S. \exists l. palgebra\text{-}embed l = S \wedge S \subseteq Path\}$$

The set family  $PA$  can be understood as consisting of only those sets of valid infinite executions which are supported by some list of finite executions.

We intend to base the measure of a set  $S \in PA$  on the measures of its supporting lists. For this purpose, the measure  $\mu_0$  is defined as:

$$\mu_0 l \equiv (\sum \tau \in set l. \pi \tau)$$

However, a set  $S$  may be supported by many different lists with different  $\mu_0$ -values. It is natural to define the measure of  $S$  to be the lower limit of these  $\mu_0$ -values. The following measure  $\mu_1$  is a refinement of  $\mu_0$  to serve this intention:

$$\mu_1 l \equiv \inf (\lambda r. \exists l'. palgebra\text{-}embed l = palgebra\text{-}embed l' \wedge \mu_0 l' = r)$$

Measure  $\mu_1$  will always return the measure of  $S$  properly, no matter which  $l$  among the supporting lists of  $S$  is used as argument.

Due to the restriction of Isabelle/HOL, we can not give a measure on  $PA$  explicitly. To solve this problem, the following measure  $\mu$  is defined on the type of all infinite execution sets, not just those in  $PA$ :

```
constdefs μ:: 'a seq set ⇒ real
μ S ≡ sup (λr. ∃ b. μ₁ b = r ∧ (palgebra-embed b) ⊆ S)
```

It can be shown that  $\llbracket S \in PA; S = \text{palgebra-embed}(l) \rrbracket \implies \mu(S) = \mu_1(l)$ , which means the formal definition of  $\mu$  is in accordance with our informal intentions discussed above. The definitions of  $\mu$  and  $\mu_1$  are copied from [11].

We proved that *algebra* (*Path*, *PA*), *positive* (*PA*,  $\mu$ ) and *countably-additive* (*PA*,  $\mu$ ). These are precisely the conditions required by *Carathéodory's extension theorem*. By applying the extension theorem, we get the probability space (*Path*, *sigma*(*Path*, *PA*),  $P$ ), on which we are going to prove that the set of parametrically fair executions is measurable and has probability 1.

The proof of *algebra* (*Path*, *PA*) is straightforward and skipped. The proof of *countably-additive* (*PA*,  $\mu$ ) is based on the fact that:

$$\begin{aligned} & \llbracket \text{range } f \subseteq PA; \bigwedge m. m \neq n \implies f m \cap f n = \{\}; (\bigcup n f n) \in PA \rrbracket \\ & \implies \exists N. \forall n. N \leq n \implies f n = \{\} \end{aligned}$$

The proof of this lemma is done using an argument similar to the proof of *König's Lemma*[12]. This lemma means that for any family  $f$  of subsets with  $(\bigcup i. f i) \in PA$ , there are only finite many  $f(i)$ s that are nonempty. Therefore, countable additivity is reduced to finite additivity, which is proved easily by induction.

Some routine properties of probability space are listed as below:

- *Monotonicity*: If  $A$  and  $B$  are measurable and  $A \subseteq B$ , then  $P(A) \leq P(B)$ .
- *Subadditivity*: If family  $A_i$  is measurable, then  $P(\bigcup_{i=0}^{\infty} A_i) \leq \sum_{i=0}^{\infty} P(A_i)$ .
- *Lower-Continuity*: If  $A_0 \subseteq A_1 \subseteq \dots \subseteq A_n \dots$  is a chain of measurable, then  $\lim_{i \rightarrow \infty} P(A_i) = P(\bigcup_{i=0}^{\infty} A_i)$ .
- If  $\forall i. P(f(i)) = 1$ , and  $n \neq 0$ , then  $P(\bigcap i \in \{0..<n\}. f(i)) = 1$ .

## 4 The probabilistic meaning of parametric fairness

The purpose of this section is to show that:

$$\text{set pel} \neq \{\} \implies P \{ \sigma \in \text{Path}. \text{PF CS pel } \sigma \} = 1 \quad (1)$$

which means the probability of the set of valid parametrically fair executions is 1. This result gives a probabilistic meaning to parametric fairness by showing that almost all valid probabilistic executions are parametrically fair. All the sets, which appear as arguments to  $P$  in this section, have been proved to be measurable in the probability space constructed in Section 3.

For this purpose, the following lemma is proved:

$$\text{set } \text{pel} \neq \{\} \implies \{\sigma \in \text{Path. } \text{PF } \text{CS } \text{pel } \sigma\} = (\bigcap_{\iota \in \text{set } \text{pel}} \text{Fair}_\iota \iota \text{ CS } \text{lf})$$

to reduce the calculation of  $P \{\sigma \in \text{Path. } \text{PF } \text{CS } \text{pel } \sigma\}$  to the calculation of  $P(\bigcap_{\iota \in \text{set } \text{pel}} \text{Fair}_\iota \iota \text{ CS } \text{lf})$ . The  $\text{Fair}_\iota$  represents a generalized way to present fairness[10], where fairness is defined relative to *labels*. The definition of  $\text{Fair}_\iota$  is given in Fig. 6.

enabled  $\iota \text{ cs } \gamma \tau \equiv \exists e. (\iota \in \gamma(\tau, e) \wedge (\tau, e) \in \text{cs})$

taken  $\iota \text{ cs } \gamma \tau \equiv \iota \in \gamma(\text{tl}(\tau), \text{hd}(\tau))$

$\text{fair}_\iota \iota \text{ cs } \gamma \sigma \equiv$   
 $\sigma \models \square \diamond (\lambda \sigma. \text{enabled } \iota \text{ cs } \gamma \llbracket \sigma \rrbracket_i) \longrightarrow \sigma \models \square \diamond (\lambda \sigma. \text{taken } \iota \text{ cs } \gamma \llbracket \sigma \rrbracket_{Suc(i)})$

fair L cs  $\gamma \sigma \equiv (\forall \iota \in \text{L. } \text{fair}_\iota \iota \text{ cs } \gamma \sigma)$

$\text{Fair}_\iota \iota \text{ cs } \gamma \equiv \{\sigma. \text{cs} \vdash \sigma \wedge \text{fair}_\iota \iota \text{ cs } \gamma \sigma\}$

If  $(\tau, e) = \{(Q, E). Q \tau \wedge e = (E \tau)\}$

**Fig. 6.** A general fairness notion

The definition of  $\text{Fair}_\iota$  uses  $\text{fair}_\iota$ , where  $\text{fair}_\iota \iota \text{ cs } \gamma \sigma$  means the particular execution  $\sigma$  is fair to label  $\iota$ . An infinite execution is said to be fair to a label  $\iota$  if  $\iota$  is taken infinite many times whenever it is enabled infinite many times in  $\sigma$ . The parameter  $\gamma$  in both  $\text{Fair}_\iota \iota \text{ cs } \gamma$  and  $\text{fair}_\iota \iota \text{ cs } \gamma \sigma$  is used to assign label sets to events so that the notions *enabled* and *taken* can be made precise. For any event  $e$ , expression  $\gamma(\tau, e)$  represents the set of labels assigned to  $e$  under system state  $\tau$ . A label  $\iota$  is said to be enabled in state  $\tau$  (written as  $\text{enabled } \iota \text{ cs } \gamma \tau$ ) if there exists some event  $e$  eligible to happen under state  $\tau$  and  $\iota$  belongs to  $\gamma(\tau, e)$ . A label  $\iota$  is said to be taken in state  $\tau$  if  $\iota$  is assigned to the last execution step of  $\tau$ . The last step of  $\tau$  is denoted by  $\text{hd}(\tau)$  and the state before the last step is denoted by  $\text{tl}(\tau)$ .

The expression  $\text{Fair}_\iota \iota \text{ cs } \gamma$  yields the set of infinite executions of concurrent system  $\text{cs}$ , which are fair to label  $\iota$ . The expression  $\bigcap_{\iota \in \text{set } \text{pel}} \text{Fair}_\iota \iota \text{ CS } \text{lf}$  represents executions of  $\text{CS}$  which are fair to every label  $\iota$  in the set  $\text{set}(\text{pel})$ . In the latter expression, labels are the  $(Q, E)$ -pairs contained in the list  $\text{pel}$ . A  $(Q, E)$ -pair is said to be assigned to  $(\tau, e)$  if  $Q(\tau) \wedge e = E(\tau)$  holds. This notion is reflected in the function  $\text{lf}$ , which is used as the parameter  $\gamma$ .

The next step is to show that  $P(\bigcap_{\iota \in \text{set } \text{pel}} \text{Fair}_\iota \iota \text{ CS } \text{lf}) = 1$ . Since we have  $\forall i. P(f_i) = 1 \wedge n \neq 0 \implies P(\bigcap i \in \{0..n\}. f_i) = 1$  for any set family  $f$ , it suffices to show  $P(\text{Fair}_\iota \iota \text{ CS } \text{lf}) = 1$  for any one label  $\iota$ . Equivalently, it is to show that  $P(\text{Path} - (\text{Fair}_\iota \iota \text{ CS } \text{lf})) = 0$ .

The next lemma we have proved is:

$$Path-(Fair\iota\ CS\ lf) = (\bigcup_{\tau \in \{xs. CS \vdash xs\}} \Gamma \iota\ CS\ lf\ \tau)$$

Now, we only need to show:

$$P(\bigcup_{\tau \in \{xs. CS \vdash xs\}} \Gamma \iota\ CS\ lf\ \tau) = 0$$

According to the *subadditivity* of probability measure mentioned in Section 3.3, it is sufficient to show that  $P(\Gamma \iota\ CS\ lf\ \tau) = 0$  for each finite execution  $\tau$ . The definition of  $\Gamma$  is:

$$\begin{aligned} \Gamma \iota\ cs\ \gamma\ \tau \equiv \\ \{\sigma \in \text{palg-embed } \tau. \\ (\forall i. \exists j. i \leq j \wedge \text{enabled } \iota\ cs\ \gamma\ \llbracket\sigma\rrbracket_j + (|\tau|)) \wedge \\ (\forall j \geq |\tau|. \neg \text{taken } \iota\ cs\ \gamma\ \llbracket\sigma\rrbracket_{\text{Suc } j})\} \end{aligned}$$

An infinite execution  $\sigma$  is said to be *unfair to  $\iota$  and indexed by  $\tau$*  if  $\tau$  is a prefix of  $\sigma$  and starting from the end of  $\tau$ ,  $\iota$  is not taken on  $\sigma$  while being enabled infinitely often at the same time. The expression  $\Gamma \iota\ cs\ \gamma\ \tau$  represents the set of such infinite executions. Suppose  $\sigma$  is in  $\Gamma \iota\ cs\ \gamma\ \tau$ , if we count the number of times  $\iota$  is enabled after  $\tau$ , this number will eventually transcend any natural number  $i$ . If  $UF\ \iota\ cs\ \gamma\ \tau\ i$  is the set of infinite executions (the definition is given in Fig.7), in which the number of times  $\iota$  is enabled after  $\tau$  is no less than  $i+1$ , then we have:

$$\Gamma \iota\ CS\ \gamma\ \tau \subseteq UF\ \iota\ CS\ \gamma\ \tau\ i$$

from this, we have  $P(\Gamma \iota\ CS\ lf\ \tau) \leq P(UF\ \iota\ CS\ lf\ \tau\ i)$ . If we can further prove that  $\lim_{i \rightarrow \infty} P(UF\ \iota\ CS\ lf\ \tau\ i) = 0$ , then we can have  $P(\Gamma \iota\ CS\ lf\ \tau) = 0$ .

Since all  $UF$ -sets are in  $\text{sigma}(Path, PA)$ , and are not supported by finite sets of finite executions. This makes  $UF$ -sets difficult to deal with. The set function  $BUF$  is defined in Fig. 7 to solve this problem, where the definition limits the value of  $k$  to the parameter  $up$ , so that every  $BUF\ \iota\ CS\ lf\ \tau\ up\ i$  is supported by a corresponding finite set  $SUF\ \iota\ CS\ lf\ \tau\ up\ i$  which consists of finite executions. Therefore, we have

$$P(BUF\ \iota\ CS\ lf\ \tau\ up\ i) = (\sum \tau \in SUF\ \iota\ CS\ lf\ \tau\ up\ i. \pi(\tau)) \quad (2)$$

Every finite execution in  $SUF\ \iota\ CS\ lf\ \tau\ up\ (i+1)$  is extended from some  $\tau_1 \in SUF\ \iota\ CS\ lf\ \tau\ up\ i$  by avoiding all the events to which  $\iota$  is assigned. Due to the avoidance of these events and (2), we managed to prove that:

$$P(BUF\ \iota\ CS\ lf\ \tau\ up\ (i+1)) \leq (1 - bnd) * P(BUF\ \iota\ CS\ lf\ \tau\ up\ i) \quad (3)$$

```

e-at  $\iota$  cs  $\gamma$   $\sigma$  j k  $\equiv$  (if enabled  $\iota$  cs  $\gamma$   $\llbracket \sigma \rrbracket_{j+k}$  then 1 else 0)

t-at  $\iota$  cs  $\gamma$   $\sigma$  j k  $\equiv$  (if taken  $\iota$  cs  $\gamma$   $\llbracket \sigma \rrbracket_{Suc(j+k)}$  then 1 else 0)

cnt-e  $\iota$  cs  $\gamma$   $\sigma$  j 0 = e-at  $\iota$  cs  $\gamma$   $\sigma$  j 0
cnt-e  $\iota$  cs  $\gamma$   $\sigma$  j (Suc i) = e-at  $\iota$  cs  $\gamma$   $\sigma$  j (Suc i) + cnt-e  $\iota$  cs  $\gamma$   $\sigma$  j i

cnt-t  $\iota$  cs  $\gamma$   $\sigma$  j 0 = t-at  $\iota$  cs  $\gamma$   $\sigma$  j 0
cnt-t  $\iota$  cs  $\gamma$   $\sigma$  j (Suc i) = t-at  $\iota$  cs  $\gamma$   $\sigma$  j (Suc i) + cnt-t  $\iota$  cs  $\gamma$   $\sigma$  j i

UF  $\iota$  cs  $\gamma$   $\tau$  i  $\equiv$   $\{\sigma \in \text{palg-embed } \tau. \exists k. \text{enabled } \iota \text{ cs } \gamma \llbracket \sigma \rrbracket_{((|\tau|)+k)} \wedge$ 
cnt-e  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = Suc i  $\wedge$  cnt-t  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = 0\}

BUF  $\iota$  cs  $\gamma$   $\tau$  up i  $\equiv$   $\{\sigma \in \text{palg-embed } \tau. \exists k < \text{up}. \text{enabled } \iota \text{ cs } \gamma \llbracket \sigma \rrbracket_{((|\tau|)+k)} \wedge$ 
cnt-e  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = Suc i  $\wedge$  cnt-t  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = 0\}

SUF  $\iota$  cs  $\gamma$   $\tau$  up i  $\equiv$   $\{\llbracket \sigma \rrbracket_{((|\tau|)+Suc k)} | \sigma k. \sigma \in \text{palg-embed } \tau \wedge k < \text{up} \wedge$ 
enabled  $\iota$  cs  $\gamma$   $\llbracket \sigma \rrbracket_{((|\tau|)+k)} \wedge$ 
cnt-e  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = Suc i  $\wedge$  cnt-t  $\iota$  cs  $\gamma$   $\sigma$  (| $\tau$ |) k = 0\}

```

**Fig. 7.** Definitions of sets of unfair executions

This together with the lemma  $\lim_{up \rightarrow \infty} P(BUF \iota cs \gamma \tau up i) = P(UF \iota cs \gamma \tau i)$  finally leads to the proof of

$$P(UF \iota CS lf \tau (i+1)) \leq (1 - bnd) * P(UF \iota CS lf \tau i),$$

which entails that  $\lim_{i \rightarrow \infty} P(UF \iota CS lf \tau i) = 0$ .

Packing all the above up, the final result in equation (1) is obtained.

## 5 Related works

Paulson's inductive approach for protocol verification[13] has been used to verify fairly complex security protocols [7, 8]. We have proposed an extension[1] to the inductive approach, which has been applied to several applications[2–4]. This paper aims for providing a probabilistic basis for our extension.

Probability space constructions for I/O automata can be found in[12, 14]. However, the execution sequence for I/O automata is an alternation of states and events, which is different from the pure event sequence used in our approach. This difference makes such works unsuitable for our purpose.

The probability space construction in this paper is heavily inspired by Hurd's construction of probability space over sets of 0-1 sequences[11], but our construction is more general in the following senses:

- Our execution sequence contains events, the type of which could be very rich, while Hurd’s sequence contains only 0 and 1. Because of this difference, the *canonical form inductive principle* in [11] does not hold anymore, this makes the proofs of canonical properties significantly more complicated.
- In our construction, the probability of the event  $e$  happening under system state  $\tau$  is given by  $R(\tau, e)$ , which is varying from state to state. Because of this, the measure on finite execution  $\tau_1 @ \tau_2$  is no longer distributive, i.e.  $\pi(\tau_1 @ \tau_2) = \pi(\tau_1) \times \pi(\tau_2)$  no longer holds. We have to find new proofs for lemmas which depend on this distributive property and these new proofs tend to be more complicated.

Generally, to deal with these differences, we have to use different techniques. Although many lemmas look very similar, their proofs are actually very different and much more complicated in our work. Another difference is that we define measure space as a triple  $(U, F, Pr)$ , where the  $U$  is the explicitly given base set. We do not follow Hurd’s treatment to represent  $U$  as the *UNIV*-set of the execution sequence type, because in our setting the base set contains only valid infinite executions, therefore, we need the additional  $U$  to make this clear. Additionally, our treatment seems more standard than Hurd’s, because standard mathematics treatment of probability theory is typeless.

Stefan Richter [15] ported some of Hurd’s work to Isabelle/HOL for the purpose of probabilistic algorithm verification. The definition of *sigma* and ten of our lemmas are copy-and-modified from Richter’s work, the modification is due to the above mentioned difference in the definition of measure space. This overlapped part consists only of a very small part of our formalization. Additionally, we have given a full Isabelle proof of the *Carathéodory’s extension theorem*, which is absent in Stefan Richter’s work.

As discussed in [16], there are many fairness notions with *weak fairness* and *strong fairness* as the standard ones. Nonstandard fairness notions, such as extreme fairness[5] and  $\alpha$ -fairness[9], are introduced to reflect more adequately the underlying probabilistic execution, so that liveness proofs can be simplified. Baier proposed a general notion of fairness[10] which subsumes all existing fairness notions. This paper shows that parametric fairness is just another instance of this general fairness notion.

While standard fairness notions can be expressed directly using LTL, extreme fairness and  $\alpha$ -fairness and our parametric fairness can not. Therefore, existing formulation of both extreme fairness and  $\alpha$ -fairness are extra-LTL, at meta level, not mechanized. In this paper, LTL is em-

bedded in Isabelle/HOL, therefore HOL serves as a mechanized meta language, in which nonstandard fairness notions can be expressed as well as its probabilistic model. We work under the belief that a mechanized meta theory may yield more reliability, maintainability and flexibility, as argued in Müller's thesis[17].

Compared with [17], embeddings of temporal logic are very similar. However, our method uses a much simpler system model, and we believe this simpler model is adequate and more convenient to use in practice, as shown in the works[13, 7, 8, 2–4]. Work [17] deals with standard fairness notions, while this paper deals with nonstandard fairness. Therefore, the liveness proof method is inherently different.

## 6 Conclusion

Inductive protocol verification is a method worth further extending. This paper together with [1–4] makes such an extension sound and practical. Liveness results usually are obtained using model-checking techniques [18]. Compared with the usual model-checking approach, our liveness verification method is based on theorem proving. It has the drawback of being less automatic, but it also has the advantage of not suffering from state explosion. Theorem proving based methods can verify systems of arbitrary large size, whereas model-checking based methods usually can only verify systems up to a limited size. By further automating, our method for liveness proof could become a promising alternative to model-checking.

In this paper, a probability space is established on the set of valid infinite executions of a concurrent system. It is then proved that the set of parametrically fair executions has probability 1 in this probability space. This result assigns a meaning to the conception of *Parametric Fairness* we proposed in [1], i.e. almost all valid infinite executions are *Parametrically Fair*. Therefore, Parametric Fairness is a reasonable fairness notion which captures the non-deterministic nature of concurrent execution.

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